

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Yoshida, et al

Serial No.: 08/929,263

Filed: 09/11/97

Docket No. TIJ-23462

Art Unit: 1104

Examiner: TBD

Title: FLASH EEPROM MEMORY ARRAY HORIZONTALLY DIVIDED INTO SECTORS
FOR ERASURE OPERATIONS



03/10/98

Assistant Commissioner for Patents
Washington, D. C. 20231

CHANGE OF CORRESPONDENCE ADDRESS

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William B. Kempler, Reg. No. 28,228

Sir:

Please change the Correspondence Address for the above-identified patent application to:

Texas Instruments Incorporated
P. O. Box 655474, M/S 3999
Dallas, TX 75251
(972) 917-5452 Phone No.
(972) 917-4418 or 4419 Facsimile

Charge any and all necessary fees to Deposit Account No. 20-0668.

Respectfully submitted,

William B. Kempler
Senior Corporate Patent Counsel
Reg. No. 28,228

Texas Instruments Incorporated
P. O. Box 655474, M/S 3999
Dallas, TX 75251
(972) 917-5452 Phone No.
(972) 917-4407 Facsimile